

FAST DIGITAL DATA RECOVERY CIRCUIT

Abstract

A digital data recovery circuit for converting an input signal into a sliced signal includes a comparing device coupled with the input signal and a reference level signal for comparing the input signal with the reference level signal and generating the sliced signal according to the result of comparison, a phase-detecting, level-determining device coupled with the comparing device for detecting the phase at which the transition of the sliced signal occurs, based on a reference clock, and generating a digital level signal according to the result of detection, and a digital-to-analog converter (DAC) coupled with the phase-detecting, level-determining device for generating the reference level signal for the comparing device according to the digital level signal.